PTO/SB/08a (08-03)

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				Application Number	TBD	
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STATEMENT BY APPLICANT				First Named Inventor	Tu, et al.	
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Sheet	1	of	2	Attorney Docket Number	TSM03-0731	

			U.S. PATENT	DOCUMENTS		
Examiner	0.11	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where	
nitials*	Cite No.1	Number - Kind Code ^{2 (I tracean)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
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Examiner Initials*		Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	П		
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Substit	ute for form 1449B/	РТО		Complete if Known		
				Application Number	TBD	
INF	ORMATIC	N DISCI	OSURE	Filing Date	Herewith	
STATEMENT BY APPLICANT				First Named Inventor	Tu, et al.	
STATEMENT OF APPLICANT			LICAIVI	Art Unit	TBD	
(Use as many sheets as necessary)				Examiner Name	TBD	
Sheet	2	of	2	Attorney Docket Number	TSM03-0731	

Examiner Initials* Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. Liu, R. et al. "Single Mask Metal-Insulator-Metal (MIM) Capacitor with Copper Damascene Metallization for Sub-0.18µm Mixed Mode Signal and System-on-a-Chip (SoC) Applications, IEEE (2000) pp. 111-113.		NON PATENT LITERATURE DOCUMENTS
LIU, R., et al., "Single Mask Metal-Insulator-Metal (MIM) Capacitor with Copper Damascene Metallization for Sub-0.18µm Mixed Mode Signal and System-on-a-Chip (SoC) Applications," IEEE (2000) pp. 111-113.	T²	Examiner Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (boo magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		LIU, R., et al., "Single Mask Metal-Insulator-Metal (MIM) Capacitor with Copper Damascene Metallization for Sub-0.18µm Mixed Mode Signal and System-on-a-Chip (SoC) Applications," IEEE (2000) pp. 111-113.

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